

FIG.1

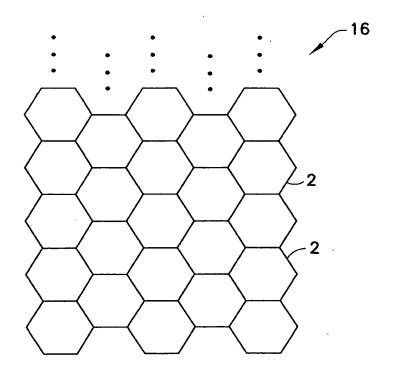
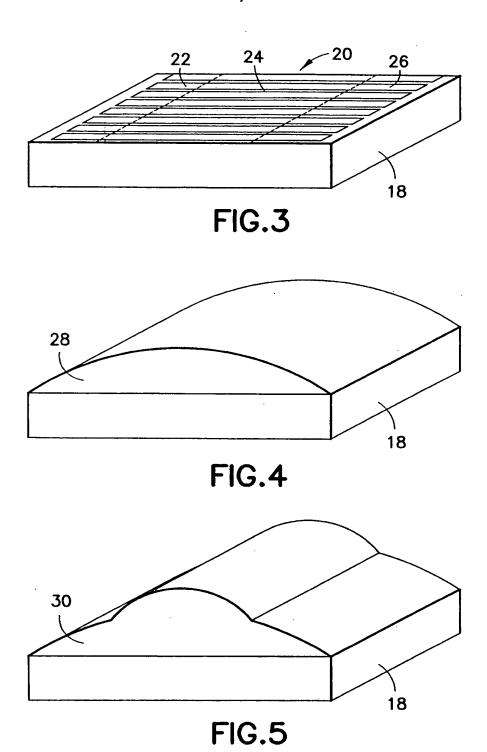
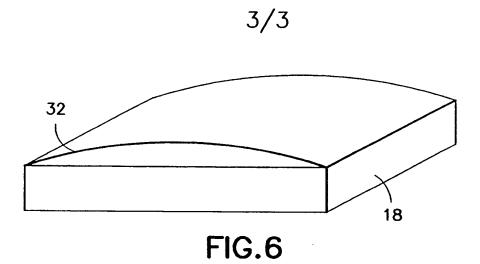


FIG.2





LENS

CMUT ELEMENTS

CMOS ELECTRONICS

PASSIVATION LAYER

SILICON SUBSTRATE

48

46

46

44

42



FIG.7

FIG.8